Abstract

The invention relates to improvements to the operation of a broadcast data receiver (BDR) and, in particular, to the provision of a video data amplifier and driver circuit for the processing of a received video data signal. The circuit includes a means for generating at least one compensatory value and preferably a multiplication factor. The compensatory value is added to the received video data signal as it passes through the circuit to form a combined signal. The combined signal can also be multiplied. The level of the compensatory value can alter with reference to changes in the environment of the operation of the circuit so as to take into account and minimise changes affecting the operation of the circuit and on the video signal.